

Electronic Acknowledgement Receipt

EFS ID:	3732067
Application Number:	10595640
International Application Number:	
Confirmation Number:	3891
Title of Invention:	Transistor and cvd apparatus used to deposit gate insulating film thereof
First Named Inventor/Applicant Name:	Toshihide Tsubata
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Filer:	Joseph Richard Keating/Rebecca Cordeau
Filer Authorized By:	Joseph Richard Keating
Attorney Docket Number:	70404.90/ma
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part/.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	70404-90-PTO-20080806-ID_S_Form_SB_08a.pdf	734320 c0b0e50b5878903019ca91e528711416 0da3a9c7	no	4

Warnings:

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2	Foreign Reference	70404-90-PTO-20080806-references-for-ids.pdf	962298 8ef1ec723288ax07e7bd0a06d81ba3b2 b04de509	no	27
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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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